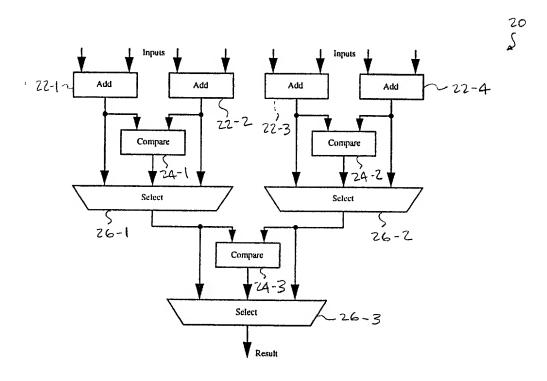
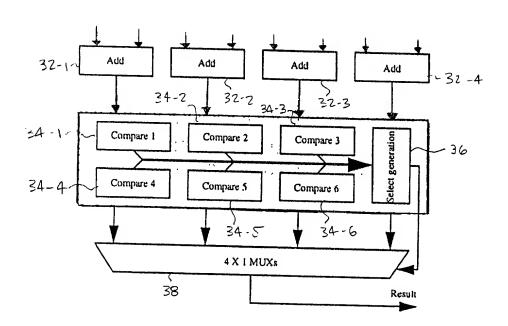


FIG. 1



F16.2



F16.3

0	a7	a6	a5	a4	a3	a2	al	a0	a a
0	b7	b6	b5	b4	b3	b2	b1	b0	b b
1	<u>c7</u>	c 6	c 5	c4	c 3	c2	<u>c1</u>	c0	a C.
1	d7	<u>d6</u>	<u>d5</u>	d4	d3	d2	d1	<u>d0</u>	a d
<u> </u>		 	<u> </u>			<u> </u>		1	BIT

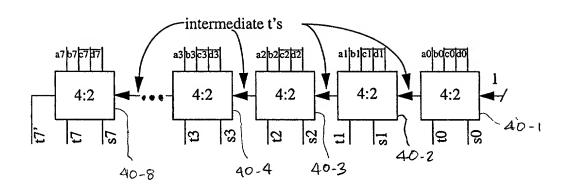
FIG. 4A

Cout(0)	Cout(1)	Remarks
0	0	p < q
0	1	$p = q \Longrightarrow p - q = 0$
1	0	Impossible
1	1	p > q

F16.4B

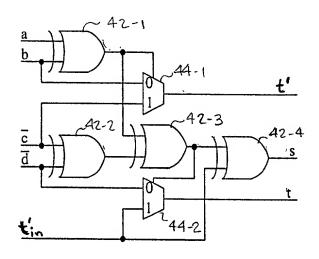
	t7'	s7	s6	s5	s4	s3	s2	s1	
1	t7	· t6	t5	t4	t3	t2	t1	t0	Ø

F16.4C



F16. 4D





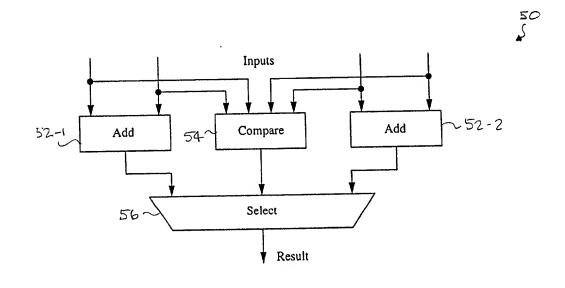
F16.4E

Input a		1	1	0	0	0	1	1	0
			_			_	_		ij
Input b		0	0	1	1	1	0	0	
Input c		1	1	1	1	0	0	0	0
Input d		0	0	0	0	1	0	0	0
The next 4 rows represent the inputs of 4:2 compression lo resented by Figure 4A	gic	, an	alc	go	us	to t	hat	re	p-
Input a	0	1	1	0	0	0	1	1	0
Input b	0	0	0	1	1	1	0	0	1
Input c	1	0	0	0	0	1	1	1	1
Input d	1	1	1	1	1	0	1	1	1
Correction bit									1
t' bits (shaded bits are internal to the network of 4:2 compressors, the unshaded bit is t7')				Ō	1			1	1
s bits		0	0	0	1	1	0	0	0
t bits	1	1	1	1	0	1	1	1	
Output of 4:2 compression network; s, t bit vectors: carry	0	0	0	0	1	1	0	0	φ
output from the MSB indicates the relative magnitudes of $a + b$ and $c + d$. In this case, Cout = 1 which implies $a + b > c + d$	1	1	1	1	0	1	1	1	

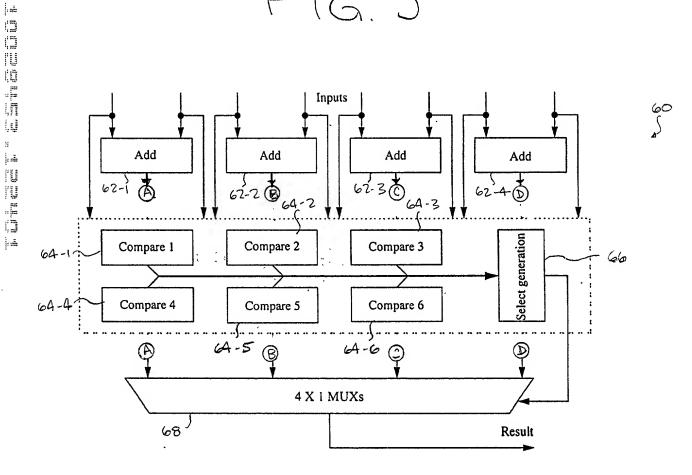
F16.4F

*				·					
Input a		1	1	0	0	0	1	1	0
Input b		0	0	1	1	1	0	0	1
Input c		1	1	1	1	0	0	0	0
Input d		0	0	0	0	1	1	1	1
The next 4 rows represent the inputs of 4:2 compression logic, analogous to that represented by Figure 4A									
Input a	0	1	1	0	0	0	1	1	0
Input b	0	0	0	1	1	1	0	0	1
Input c	1	0	0	0	0	1	1	1	1
Input d	1	1	1	1	1	0	0	0	0
Correction bit						L		L	1
t' bits (shaded bits are internal to the network of 4:2 compressors, the unshaded bit is t7')				0	1	i	1	1	1
s bits		0	0	0	1	1	1	1	1
t bits	1	1	1	1	0	0	0	0	
Output of 4:2 compression network; s, t bit vectors: carry	0	0	0	0	1	1	1	1	φ
output from the MSB indicates the relative magnitudes of $a + b$ and $c + d$. In this case, Cout = 0 which implies $a + b \le c + d$ (Conditional carries Cout(0) = 0 and cout(1) = 1 in this case)	1	1	1	1	0	0	0	0	

F16. 4G



F16.5



F16.6

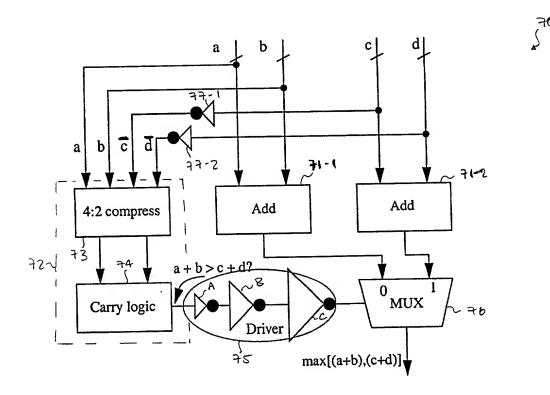


FIG. 7A

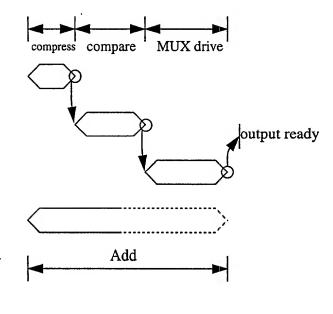
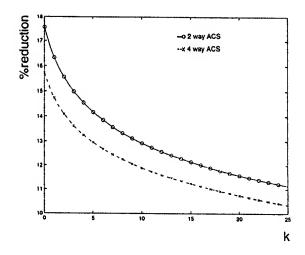
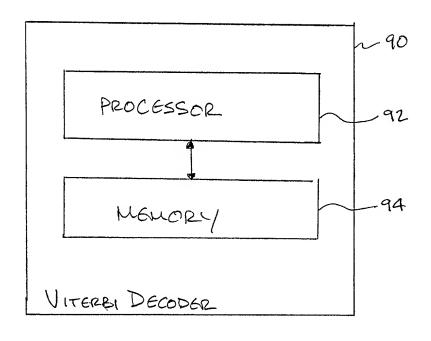


FIG. 7B



F16.8



F16.9